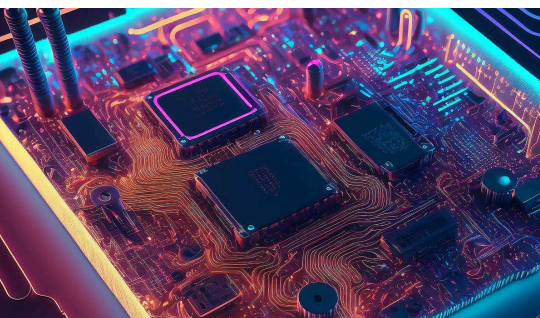# Enabling Excellence in Embedded Systems Design and VLSI Training

About Us

Sandeepani is the Training Division of CoreEL Technologies and the pioneer in VLSI & Embedded Systems training in India.
- Authorized Training Provider for AMD-Xilinx
- Cutting-edge EDA tools and FPGA facilities
- Trained over 5000+ professionals from various industries

Our Offerings

- Corporate Training
- Technical Induction for New College Graduates
- Integrated Training and Placement Assistance Programs
- Faculty Development Programs
- Career Awareness Programs

Our Vision

To be the leading technological institute for organizations, empowering engineers with cutting edge knowledge and innovative solutions through a transformative learning experience

Why Sandeepani?

- Pioneer in VLSI/Embedded System Training in India
- Authorized Training Provider for AMD-Xilinx
- AMD-Xilinx Certified Faculty
- Access to updated and high quality material, labs and tools
- Faculty experienced with training engineers at all levels

Our Customers

AMD Authorized Training Provider

Technical Programs

Languages

✓ Designing with Verilog

✓ Designing with VHDL

✓ Advanced VHDL

✓ Designing with SystemVerilog

✓ Verification with SystemVerilog

Embedded and SoCs

✓ Migrating to the Vitis Embedded Software Development IDE Workshop

✓ Zynq UltraScale+ MPSoC Courses for Hardware Designer, Software Developer & System Architect

✓ Zynq SoC System Architectures

✓ Embedded Design with PetaLinux Tools

✓ Embedded Systems Design

FPGA

✓ Designing FPGAs using the Vivado Design Suite 1-4

✓ Designing with 7 Series

✓ UltraFast Design Methodology

✓ Designing with the UltraScale and UltraScale+ Architectures

✓ Designing with the IP Integrator Tool

✓ Design Closure Techniques

✓ Timing Closure Techniques

✓ Vivado Design Suite Advanced XDC and Timing Analysis for ISE Users

✓ Designing with Dynamic Function eXchange (DFX) Using the Vivado Design Suite

Adaptive SoCs

✓ Designing with the Versal ACAP: Architecture and Methodology

✓ Designing with the Versal ACAP: Network on Chip

Connectivity

✓ Designing with the Zynq UltraScale+ RFSoC

AI

✓ Using Vision-based Applications with the Kria KV260 Vision AI Starter Kit & System-on-Module

DSP

✓ High-Level Synthesis with the Vitis HLS Tool

✓ DSP Design Using System Generator

Contact us for customized training solutions

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